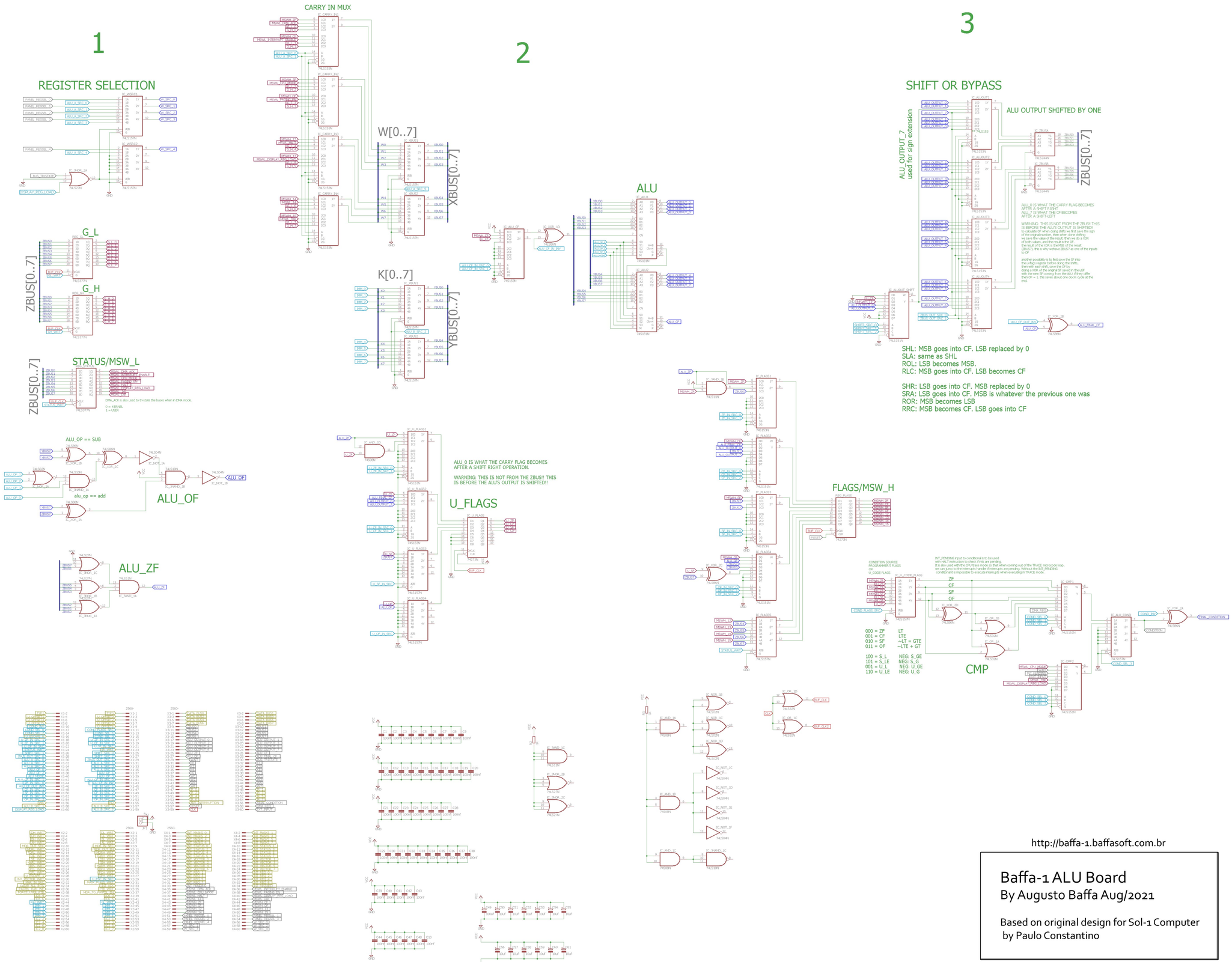
U_AD[0..13]
ADDER
U_ADDER[0..13]
DATA_BUS[0..7]
IR
if type = branch, and condition = false, then next = +1
Typ1Typ0
00 offset
01 branch
10 pre-fetch
11 post_fetch
X1, X2 and X3 are a the bottom of PCB
This allows access from bottom of the assembled machine
Baffa-1 Controller Board
By Augusto Baffa Jul/2021
Based on original design for Sol-1 Computer
by Paulo Constantino
http://baffa-1.baffasoft.com.br
MICROCODE SEQUENCING
U-ADDRESS MUX
U-ADDRESS
U_ADDER[0..13]
U_AD[0..13]
FETCH AT 0x10
TRAP AT 0x20
MICROCODE 0 TO 63
IS RESERVED FOR CPU STARTUP.



http://baffa-1.baffasoft.com.br

Baffa-1 ALU Board

By Augusto Baffa Aug/2021

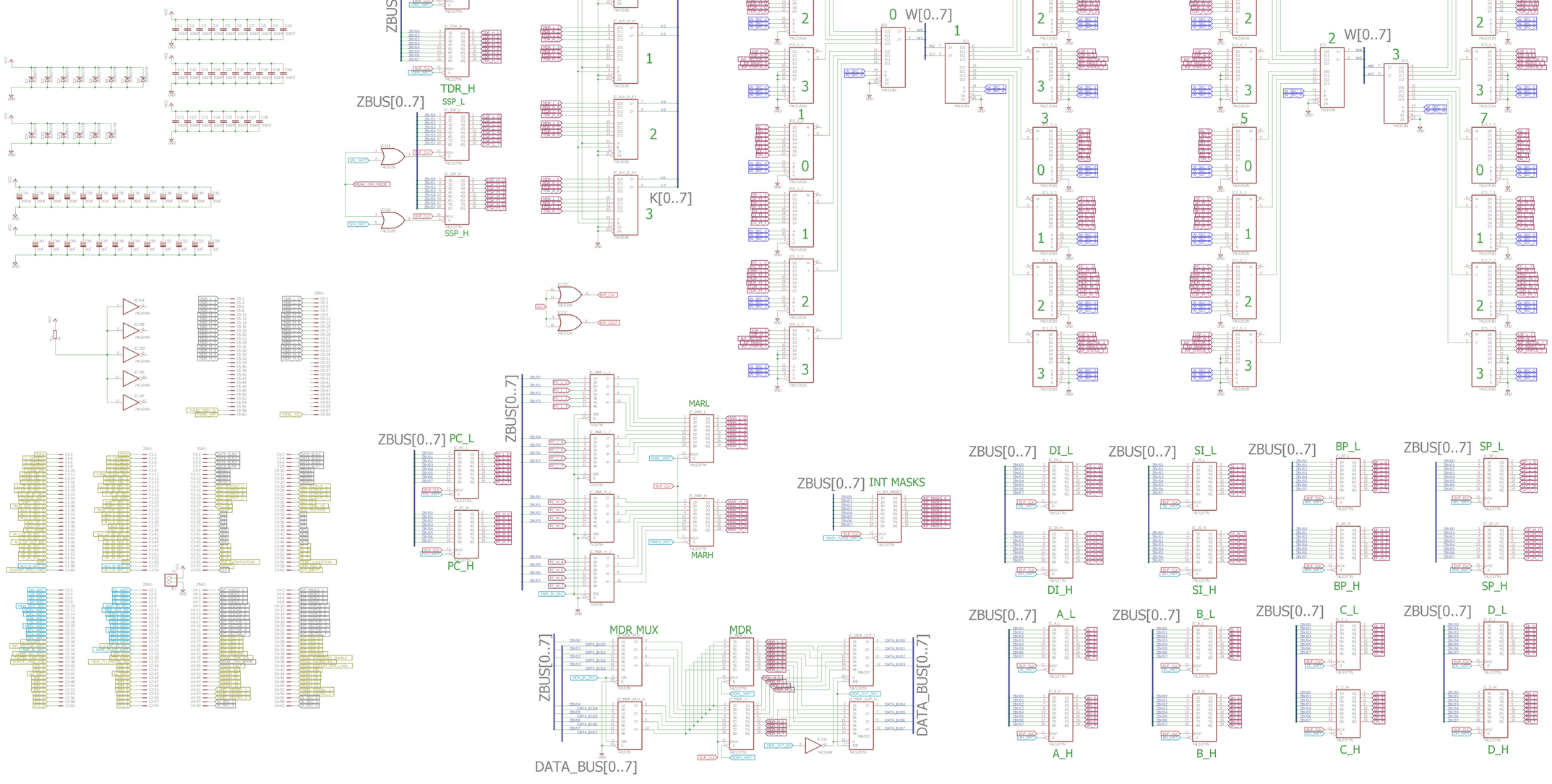
Based on original design for Sol-1 Computer by Paulo Constantino

Baffa-1 Registers Board

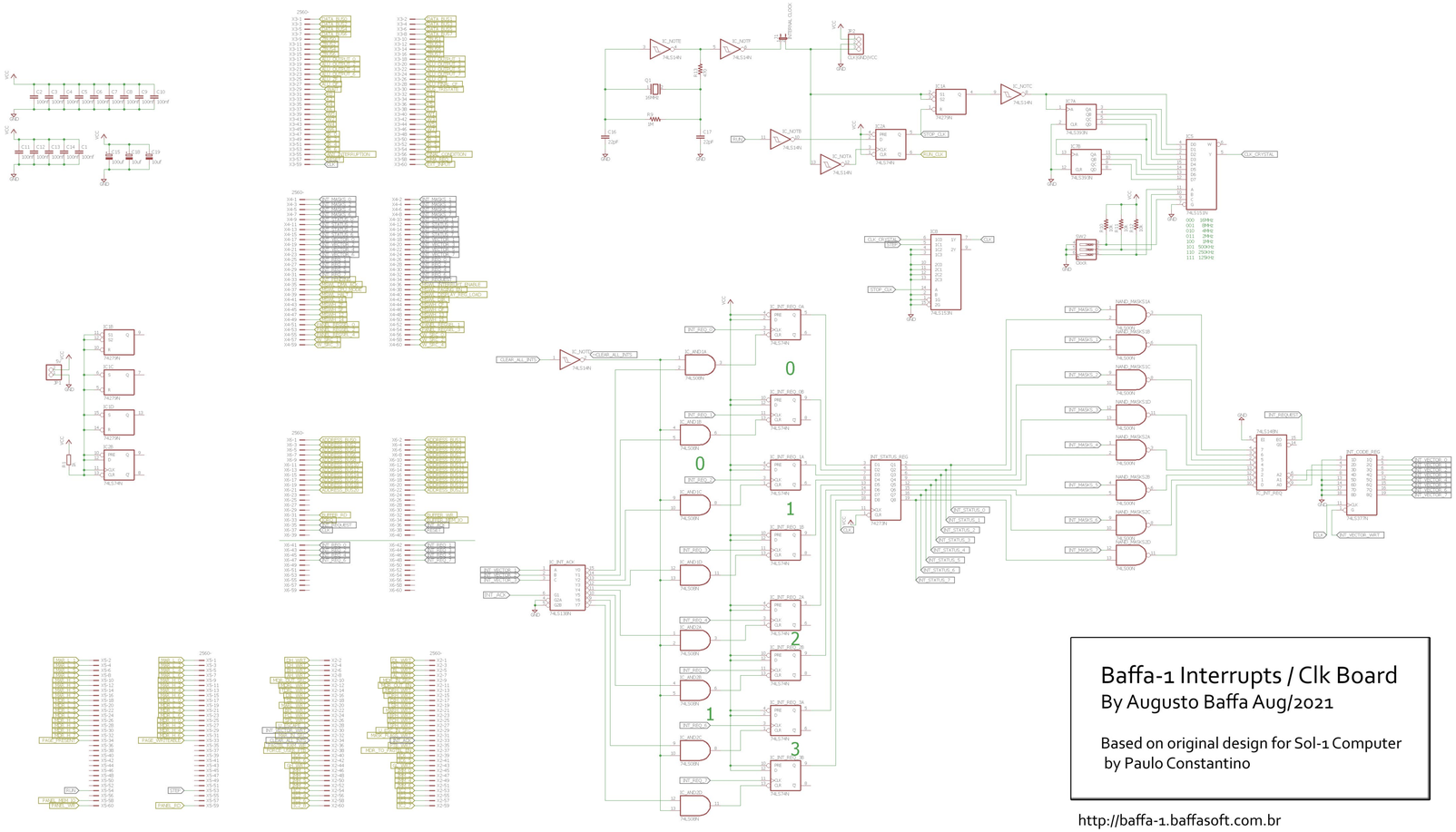
By Augusto Baffa Jul/2021

Based on original design for Sol-1 Computer
by Paulo Constantino

http://baffa-1.baffasoft.com.br



MEM_DATA_BUS[0..7]
MEM_ADDRESS_BUS[0..21]
DATA_BUS[0..7]
ADDRESS_BUS[0..21]
PTB
ZBUS[0..7]
Baffa-1 Memory / Bios Board
By Augusto Baffa Aug/2021
Based on original design for Sol-1 Computer
by Paulo Constantino
http://baffa-1.baffasoft.com.br



Baffa-1 Interrupts / Clk Board

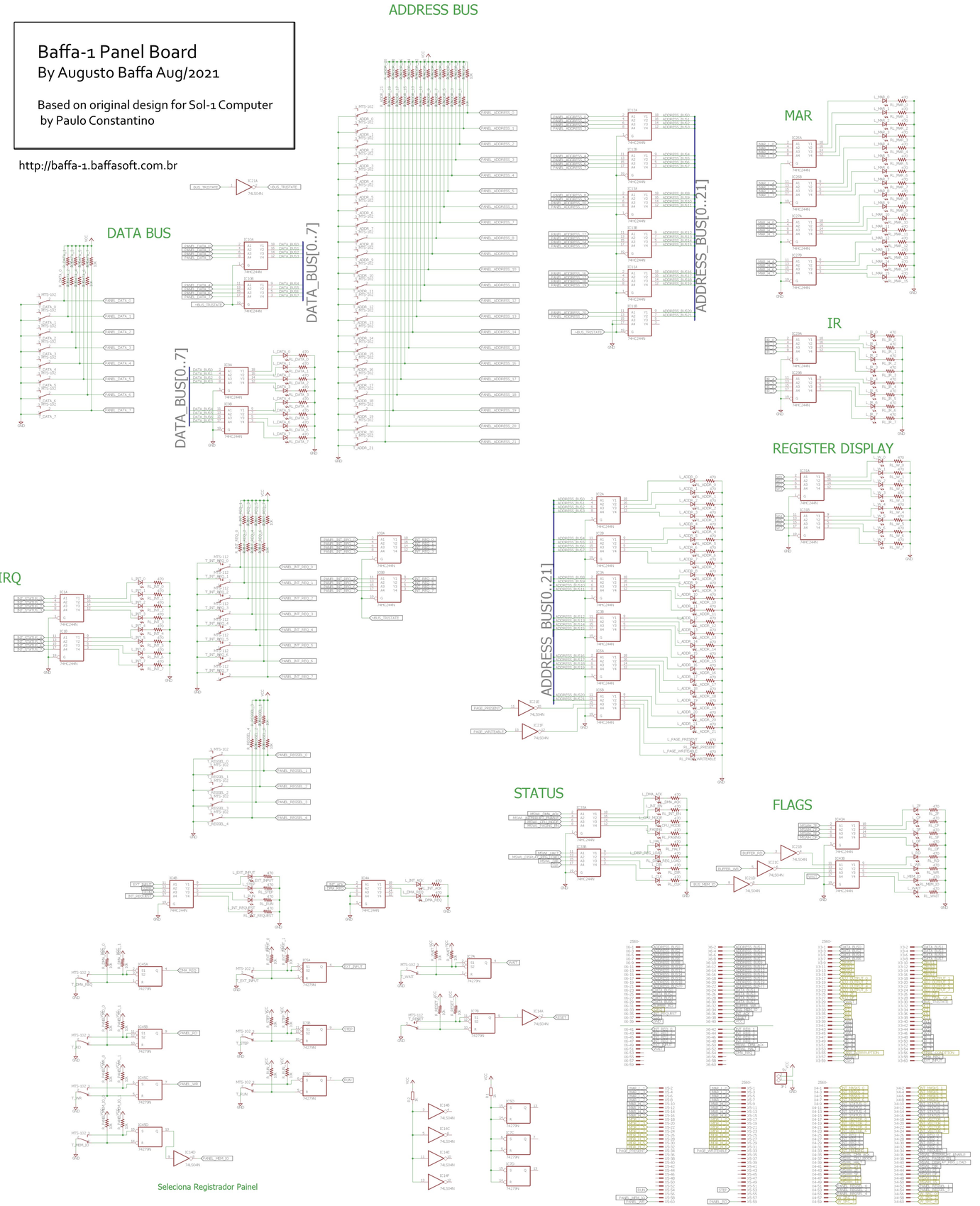
By Augusto Baffa Aug/2021

Based on original design for Sol-1 Computer
by Paulo Constantino

http://baffa-1.baffasoft.com.br

Baffa-1 Panel Board

By Augusto Baffa Aug/2021

Based on original design for Sol-1 Computer
by Paulo Constantino

http://baffa-1.baffasoft.com.br